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10/767,562	01/29/2004	Gerd Knappe	304-820	5090
30448	7590	02/16/2006	EXAMINER	
AKERMAN SENTERFITT P.O. BOX 3188 WEST PALM BEACH, FL 33402-3188			KRAMSKAYA, MARINA	
			ART UNIT	PAPER NUMBER
			2858	

DATE MAILED: 02/16/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Application No.

**10/767,562**

<b>Applicant(s)</b>
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KNAPPE ET AL.

**Examiner**

## Marina Kramskaya

<b>Art Unit</b>
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2858

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
  - If no period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 28 November 2005.  
2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.  
3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-12 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-12 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 28 November 2005 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \*    c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## **DETAILED ACTION**

### ***Drawings***

1. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they include the following reference character(s) not mentioned in the description: resistors **R1-R5**. Corrected drawing sheets in compliance with 37 CFR 1.121(d), or amendment to the specification to add the reference character(s) in the description in compliance with 37 CFR 1.121(b) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

### ***Specification***

2. The disclosure is objected to because of the following informalities: it is improper to make reference to claims by claim number as in paragraph [0012] on page 3.

Appropriate correction is required.

***Claim Rejections - 35 USC § 112***

3. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

4. Claim 1 rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. The newly added limitation of "one low drain-source resistor per said sensor" does not have support in the specification. The specification provides support for the original limitation of a "low drain-source resistance." Further, the replacement FIG. 1, shows resistors R1-R4 connected to the Gate of the MOSFET. No support in the disclosure has been provided for a resistor to be connected between the Drain and the Source of the MOSFET.

***Claim Rejections - 35 USC § 102***

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States

only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

For the purpose of this examination, the “one low drain-source resistor” will be interpreted as a “low drain-source resistance”, since a resistance is provided between the drain and source of a MOSFET.

6. Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Kawate et al., US 6,642,711.

As per Claim 1, Kawate discloses a circuit arrangement with several inductively operating sensors (Sense Coil 1-4), said circuit arrangement having

- switching means (groups of T3 & T4, FIG. 2, part 1),
- control means (Main Voltage Regulator, FIG. 2, part 2) for said sensors and
- evaluating means (Comparator, FIG. 2, part 1) for signals generated by said sensors as a response to said control means and
- wherein said control means and said evaluating means are electrically connected by said switching means to in each case one said sensor (see combination FIG. 2, parts 1 & 2),
- wherein said switching means comprise a single MOSFET (T4) with a low drain-source resistance. It is inherent for a MOSFET to have a low drain-source resistance during conduction.

***Claim Rejections - 35 USC § 103***

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kawate in view of Hohl, US 6,724,198.

Kawate discloses an inductive circuit arrangement as applied to Claim 1, above.

Kawate does not disclose providing precisely one switching means per sensor.

Hohl discloses providing precisely one switching means (50) per inductive sensor (20), FIG. 2A.

Therefore, it would have been obvious to a person of ordinary skill in the art to provide precisely one switch per inductive sensor, as taught by Hohl, in order to enable oscillation (Hohl: column 4, lines 35-45).

9. Claims 3-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kawate in view of James, US 6,512,370.

As per Claim 3 and 4, Kawate discloses an inductive circuit arrangement as applied to Claim 1, above.

Kawate does not disclose circuit arrangement having resonant circuit capacitors, with a single resonant circuit capacitor being a first resonant circuit capacitor and being connectable by a switching means parallel to in each case all said sensors for producing a measuring frequency and a second resonant circuit capacitor parallel to said first resonant circuit capacitor, and switches are provided in order to switch on and off said different resonant circuit capacitors.

James discloses an inductive sensing (inductive sensor **12**) circuit arrangement a first (**C1**) and second (**C2**) resonant capacitors (part of resonant circuit **17**), connected via switching means (**21**) to the sensor and wherein the second capacitor is connected in parallel to the first capacitor (FIG. 5).

Therefore, it would have been obvious to a person of ordinary skill in the art to employ a resonant circuit with parallel resonant capacitors, as taught by James, in order to produce resonating frequencies without changing the current levels.

As per Claim 5, Kawate in view of James discloses the circuit as applied to Claim 4, above.

Kawate does not expressly teach the switching on and off of the resonant circuit capacitors to produce differing measuring frequencies.

James discloses a plurality of measuring frequencies that are produced by the circuit arrangement, wherein switching on and off of said resonant circuit capacitors produces different measuring frequencies (column 6, lines 54-61).

Therefore, it would have been obvious to a person of ordinary skill in the art to utilize a circuit arrangement wherein switching on and off of said resonant circuit capacitors produces different measuring frequencies, as taught by James, in the sensing circuit of Kawate, in order to utilize the frequency difference to determine the gain.

Kawate, as modified by James, does not expressly teach producing a difference of at least 8% between measuring frequencies.

However, it would have been obvious to a person of ordinary skill in the art at the time the invention was made, to arrive at the difference of at least 8% by routine experimentation (see MPEP 2144.05), in order to provide for a greater span of frequencies at which measurements may be performed.

10. Claim 6-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kawate in view of Smolenski et al, US 6,350,971.

As per Claim 6, Kawate discloses an inductive sensor circuit arrangement as applied to Claim 1, above.

Kawate does not disclose an inductive sensor circuit for detection of a pot or saucepan in a cooking zone.

Smolenski discloses an inductive sensor circuit, wherein said sensors are pot or saucepan (vessel **120**) detection sensors (**160** inductive loop) in a cooking zone (column 3, lines 25-34).



Therefore, it would have been obvious to a person of ordinary skill in the art to use an inductive position sensor to detect the presence of a cooking vessel, as taught by Smolenski, in order to detect when a range top needs to be heated.

As per Claim 7, Kawate further discloses the circuit arrangement, wherein the sensor is a wire loop having a few turns (see FIG. 2, part 1, Sense coils 1-4).

11. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kawate in view of Uber, III et al., US 6,353,324.

Kawate discloses a method for operating a circuit arrangement with several inductively operating sensors (Sense Coil 1-4), having

- switching means (groups of T3 & T4),
  - control means (Main Voltage Regulator) for said sensors and
  - evaluating means (Comparator) for signals, which are generated by said sensors as a response to said control means and
  - said control means and evaluating means are electrically connected by said switching means to in each case one said sensor (see combination FIG. 2, parts 1 & 2),
  - said switching means being a MOSFET (groups of transistors T3 & T4) with a low drain-source resistance, (It is inherent for a MOSFET to have a low drain-source resistance during conduction),
- the method comprising

- evaluating the signals generated by said sensors with said evaluating means.

Kawate does not disclose readjusting a gate control voltage of the MOSFET so as to give a frequency which is constant with varying temperature.

Uber discloses a method of inductively sensing, wherein a gate control voltage (+Vs) of the MOSFET (169) is readjusted so as to give a frequency which is constant with varying temperature (column 22, lines 46-48).

Therefore, it would have been obvious to a person of ordinary skill in the art to provide a MOSFET with gate control voltage, as taught by Uber, in order to provide temperature compensation for the sensing method.

12. Claims 9 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kawate in view of Uber as applied to claim 8 above, and further in view of James.

As per Claim 9, Kawate in view of Uber disclose a method of operating a circuit arrangement with several inductively operating sensors as applied to claim 8, above

Kawate, as modified, does not disclose an operation, which takes place with two measuring frequencies.

James discloses a method of inductive sensing, wherein operation takes place with varying measuring frequencies (ABS., lines 2-4).

Therefore, it would have been obvious to a person of ordinary skill in the art to set the varying frequency, as taught by James, during operation to two measuring

frequencies, in order to indicate the magnitude and polarity of the magnetic field in the inductive sensor.

As per Claim 11, Kawate in view of Uber disclose a method of operating a circuit arrangement with several inductively operating sensors as applied to claim 8, above.

Kawate does not disclose a method of inductive sensing, wherein two different capacitors are connected in parallel to one said sensor as resonant circuit capacitors and are operated with different measuring frequencies.

James discloses a method of inductive sensing, wherein two different capacitors are connected in parallel (C1 and C2) and operation takes place with varying measuring frequencies (ABS., lines 2-4).

Therefore, it would have been obvious to a person of ordinary skill in the art to operate the capacitors with difference measuring frequencies, in order to indicate the magnitude and polarity of the magnetic field in the inductive sensor.

13. Claim 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kawate and Uber as applied to claim 8 above, and further in view of Smolenski.

Kawate in view of Uber disclose the method of operating a circuit arrangement with several inductively operating sensors.

Kawate does not disclose a method of inductive sensing, wherein a probability is calculated and it is established whether or not a saucepan is present by averaging over numerous measurements

Smolenski discloses an inductive sensing method (**160** inductive loop), wherein by averaging over numerous measurements a probability is calculated and by means (by processor **170**) thereof it is established whether or not a saucepan (vessel **120**) is present (column 3, lines 25-34).

Therefore, it would have been obvious to a person of ordinary skill in the art to use an inductive position sensor to detect the presence of a cooking vessel, as taught by Smolenski, in order to detect when a range top needs to be heated.

14. Claim 12 is rejected under 35 U.S.C. 103(a) as being unpatentable over Hohl, US 6,724,198, in view of James, US 6,512,370, and Kawate et al., US 6,642,711.

Hohl discloses a circuit arrangement with several inductively operating sensors (coil 1-coil n, **20**), said circuit arrangement comprising:

- switching means (BJT's ,**50**);
- control means (feedback control **32**) for said sensors; and
- evaluating means (**30**) for signal generated by said sensors as a response to said control means,

wherein said control means (**32**) and said evaluating means (**30**) are electrically connected by said switching means to in each case one said sensor (see FIG. 1),

wherein said switching means comprise a BJT,

wherein precisely one switching means is provided per sensor (one switch **50** per one sensor **20**), and

wherein two difference capacitors (**54** and **56**) are connected to one sensor.

Hohl does not explicitly disclose a switching means comprising a MOSFET with a low drain-source resistance, or two difference capacitors that are connected in parallel to one sensor as resonant circuit capacitors and are operable with different measuring frequencies.

Kawate disclose using a MOSFET switching means (T3) for each inductive sensor (wherein it is inherent for a MOSFET to have a low drain-source resistance during conduction).

James discloses a method of inductive sensing, wherein two different capacitors are connected in parallel (C1 and C2) and operation takes place with varying measuring frequencies (ABS., lines 2-4).

Therefore, it would have been obvious to a person of ordinary skill in the art to use a MOSFET switch, as taught by Kawate, in the sensor of Hohl, rather than a BJT switch in order to achieve high switching rate with low switching losses. Further, it would have been obvious to a person of ordinary skill in the art to operate the capacitors with difference measuring frequencies, as taught by James, in the sensor of Hohl, in order to indicate the magnitude and polarity of the magnetic field in the inductive sensor.

### ***Response to Arguments***

15. Applicant's arguments filed 12/02/2005 have been fully considered but they are not persuasive.

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16. In response to applicant's arguments that Kawate does not teach the claimed limitations, as currently amended, the examiner draws attention to the phrase "said switching means comprise." The term "comprise" is presumptively open-ended, see MPEP 2111.02. Hence, the switching means may contain more elements other than a single MOSFET.

17. In response to applicant's argument that there is no suggestion to combine the references, the examiner recognizes that obviousness can only be established by combining or modifying the teachings of the prior art to produce the claimed invention where there is some teaching, suggestion, or motivation to do so found either in the references themselves or in the knowledge generally available to one of ordinary skill in the art. See *In re Fine*, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988) and *In re Jones*, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992). In this case, it would have been obvious to a person of ordinary skill in the art to provide precisely one switch per inductive sensor, as taught by Hohl, in the sensor of Kawate, in order to enable oscillation (Hohl: column 4, lines 35-45). Further, it would have been obvious to a person of ordinary skill in the art to employ a resonant circuit with parallel resonant capacitors, as taught by James, in the sensor of Kawate, in order to produce resonating frequencies without changing the current levels.

***Conclusion***

18. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Marina Kramskaya whose telephone number is (571)272-2146. The examiner can normally be reached on M-F 7:00-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Diane Lee can be reached on (571)272-2399. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MK

Marina Kramskaya  
Examiner  
Art Unit 2858



DIANE LEE  
SUPERVISORY PATENT EXAMINER